

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Final Office Action dated December 14, 2005 has been received and its contents carefully reviewed.

Claims 1, 7, 11, and 18 are hereby amended. Claims 1–23 are currently pending. Reexamination and reconsideration are hereby requested.

In the Office Action, claims 1–6 and 11–17 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Applicants' Related Art (hereinafter "ARA") in view of U.S. Patent Application, Publication No. 2002/0196221 by Morita (hereinafter "Morita") and further in view of U.S. Patent Application, Publication No. 2001/0038372 by Lee (hereinafter "Lee"); claims 7–10 are rejected under 35 U.S.C. § 103(a) as being unpatentable over ARA in view of Morita and further in view of Lee and U.S. Patent No. 6,853,384 to Miyata et al. (hereinafter "Miyata"); and claims 18–23 are rejected under 35 U.S.C. § 103(a) as being unpatentable over ARA, Morita, and Lee.

In the Office Action, claims 1–6 and 11–17 are rejected under 35 U.S.C. § 103(a) as being unpatentable over ARA in view of Morita and further in view of Lee. Applicants respectfully traverse the rejection of independent claim 1 and request reconsideration. Independent claim 1 is allowable in that it recites "reducing the number of bits of the source data, thereby generating a reduced-bit source data, wherein reducing the number of bits includes converting an odd source data value into an even source data value." Nothing in ARA, Morita, and Lee, alone or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicants respectfully submit that claim 1, and its dependent claims 2–6, are allowable over any combination of ARA, Morita, and Lee.

Applicants respectfully traverse the rejection of independent claim 11 and request reconsideration. Independent claim 11 is allowable in that it recites "a modulator for comparing the reduced-bit source data of a current frame with reduced bit source data of a previous frame to modulate the source data by using a preset modulated data in accordance with a result of the comparison, wherein a bit number of the reduced-bit source data of the previous frame is the

same as that of the current frame, and a bit number of the preset modulated data is more than that of the reduced-bit source data of each previous frame and current frame, and wherein the modulator replaces all of the bits of the source data with preset modulated data.” Nothing in ARA, Morita, and Lee, alone or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicants respectfully submit that claim 11, and its dependent claims 12–17, are allowable over any combination of ARA, Morita, and Lee.

In the Office Action, claims 7–10 are rejected under 35 U.S.C. § 103(a) as being unpatentable over ARA in view of Morita and further in view of Lee and Miyata. Applicants respectfully traverse the rejection of independent claim 7 and request reconsideration. Independent claim 7 is allowable in that it recites “determining whether an n - k bit source data of the current frame is identical to an n - k bit source data of the previous frame stored in the storage memory, wherein k is a positive integer less than n , and wherein the n - k bit source data corresponds to most significant n - k bits of the n bits; and supplying the n bit source data of the current frame to a liquid crystal display panel or modulating an n - k bit source data by using one of the first and second n bit modulated data in accordance with a result of the determining whether an n - k bit source data of the current frame is identical to an n - k bit source data of the previous frame, wherein modulating an n - k source data includes replacing all of the bits within the n bit source data with the n bit modulated data.” Nothing in ARA, Morita, Lee, and Miyata, alone or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicants respectfully submit that claim 7, and its dependent claims 8–10, are allowable over any combination of ARA, Morita, Lee, and Miyata.

In the Office Action, claims 18–23 are rejected under 35 U.S.C. § 103(a) as being unpatentable over ARA, Morita, and Lee. Applicants respectfully traverse the rejection of independent claim 18 and request reconsideration. Independent claim 18 is allowable in that it recites “a modulator for registering a first n bit modulated data that has a larger value than a data value of the current frame in accordance with an increase of the data value, and a second n bit modulated data that has a smaller value than the data value of the current frame in accordance with a decrease of the data value, and for supplying the source data of the current frame to the liquid crystal display panel, or modulating the n - k bit source data by using the first and second n

bit modulated data in accordance with an output of the comparator, wherein modulating includes replacing all of the bits of the source data with the n bit modulated data.” Nothing in ARA, Morita, and Lee, alone or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicants respectfully submit that claim 18, and its dependent claims 19–23, are allowable over any combination of ARA, Morita, and Lee.

Applicants believe the foregoing amendments place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

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Respectfully submitted,

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